

MASSACHUSETTS INSTITUTE OF TECHNOLOGY
Department of Electrical Engineering
And Computer Science

6.977 Semiconductor Optoelectronics – Fall 2002

Problem Set 2 – Introduction to Photodetection

Problem #1 *Electrostatics of heterojunction diodes.*

- In class, we derived an expression for the capacitance of a homojunction p-n diode, derive a similar expression for the capacitance of a GaAs-AlGaAs p-N diode. Show your work
- Sketch the charge density, electric field and band diagram for an $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ -GaAs- $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ p⁺-n-n⁺ diode. The p⁺ doping is $N_a=10^{19} \text{ cm}^{-3}$, the middle n-type doping is $N_d=10^{16} \text{ cm}^{-3}$ and the n⁺ doping is $N_d=10^{18} \text{ cm}^{-3}$. Assume that the p⁺ and n⁺ layers are thick and that the middle n-layer is 0.1 μm thick. Pay careful attention to the depletion state of the middle layer. Show your work.

Problem #2 *This problem explores the design trade-offs for high speed photodetectors.* Consider vertically illuminated homojunction GaAs p-i-n diode. Assume that the p-side is doped at $N_a=10^{19} \text{ cm}^{-3}$ and the n-side is doped at $N_d=10^{17} \text{ cm}^{-3}$. For GaAs, the static dielectric constant is $\epsilon=13 \epsilon_0$ and the saturation velocity is $v_{\text{sat}}=10^7 \text{ cm/s}$.

- Assuming that the load resistance for the photodetector is 50 Ω , calculate the RC limited 3-dB bandwidth. Plot the bandwidth (in GHz) as a function of intrinsic layer thickness – consider $d_i=0.1 - 5 \mu\text{m}$. Plot a family of curves for detector areas of 700, 1400, 5000 μm^2 .
- Replot the 3-dB bandwidth but now include the effects of finite transit time across the depletion region on the overall bandwidth. Plot for the same range of intrinsic layer thickness and diameter.
- Plot the quantum efficiency of the photodetector for the same range of intrinsic layer thickness.
- For a detector of 700 μm^2 , what is the optimal layer thickness for maximizing the bandwidth-efficiency product? What are the 3-dB bandwidth and efficiency for this design?

Problem #3 This problem explores photovoltaic devices (solar cells). Consider an $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ -GaAs- $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ p-i-n heterojunction diode. Assume that the p-side is doped at $N_a=10^{19} \text{ cm}^{-3}$ and the n-side is doped at $N_d=10^{17} \text{ cm}^{-3}$. Light at 1.5 eV is incident on the diode. The p-type is $1 \mu\text{m}$ thick, the intrinsic GaAs is $0.2 \mu\text{m}$ thick and the n-type is $1 \mu\text{m}$ thick. The light is incident on the n-side. Unless otherwise stated assume the device is at 300K.

Use SimWindows to answer the following questions.

- For 200 mW/cm^2 of incident light intensity, how much light is absorbed in the solar cell per unit area? Note: the optical power can be plotted using 'Plot selected parameter' and plotting the 'Total Incident Poynting' Vector.
- Plot the I-V characteristics for this diode with and without illumination. Notice that under illumination, there is a reverse current that flows when the voltage across the diode is positive. In this region the diode is *generating* electrical power. What is the peak power generated? What is the efficiency of converting the absorbed optical power into electrical power? What is the efficiency of converting the incident optical power into electrical power?
- The total power generated by a solar cell increases as the light intensity grows (for example on a very sunny day). Unfortunately, not all the incident light turns into electricity, some turns into heat. For higher incident optical intensity there is more heating for the cell. Determine the maximum conversion efficiency for the absorbed optical power for $T=300, 320$ and 360 K .
- Explain the dependence of the power conversion efficiency on temperature. What could improve the performance? What is the implication on the design of solar arrays? Specifically, what are the relative merits of using large areas of p-n diodes versus using a low-cost light concentrator and small areas of p-n diodes.